

TITLE: "A MEMORY DEVICE WITH AN ALTERNATING VSS INTERCONNECTION"

INVENTOR(S): Timothy Thurgate and Richard Fastow

USSN: 10/823,972 Attorney Docket #: AMD-H0546

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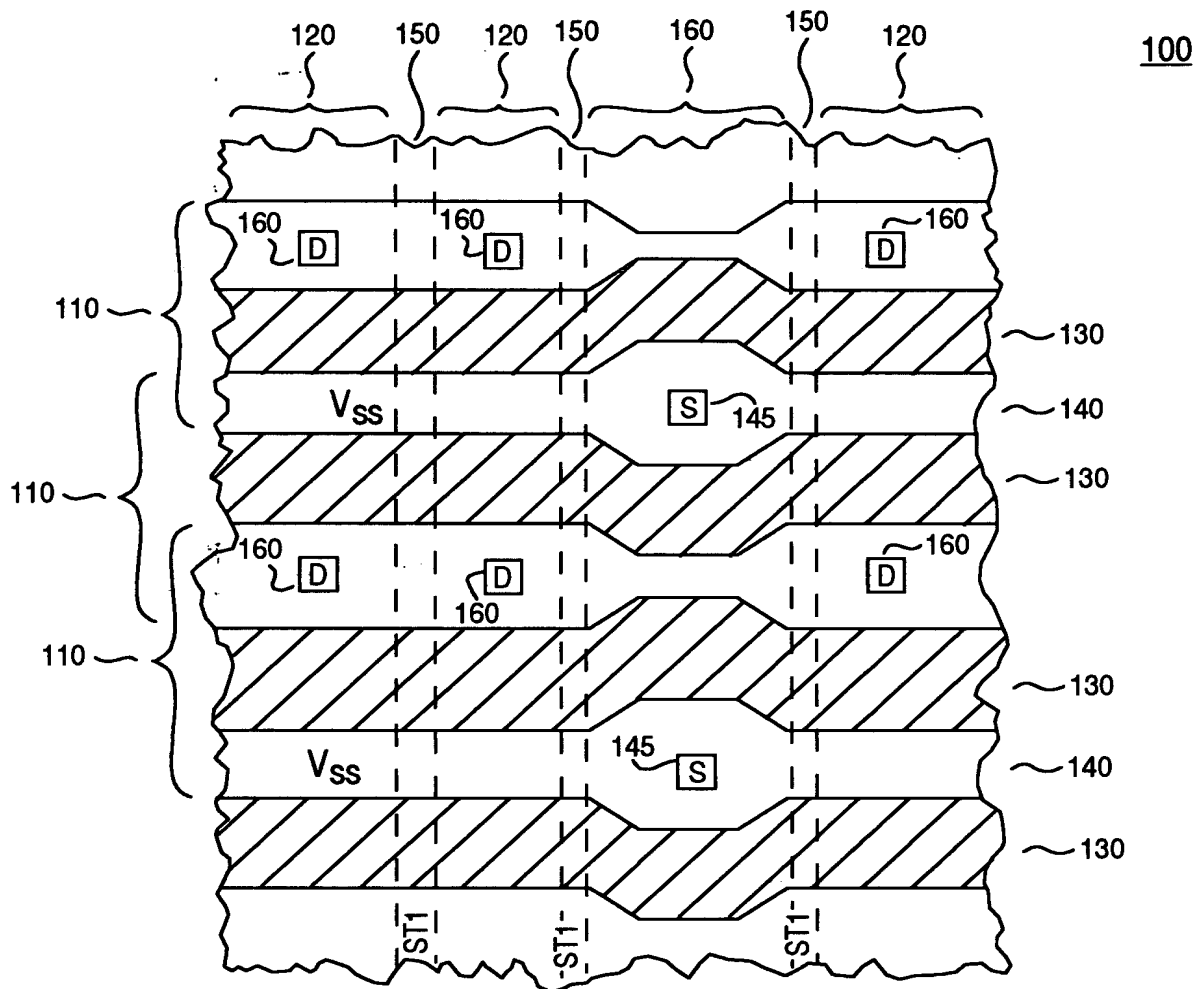
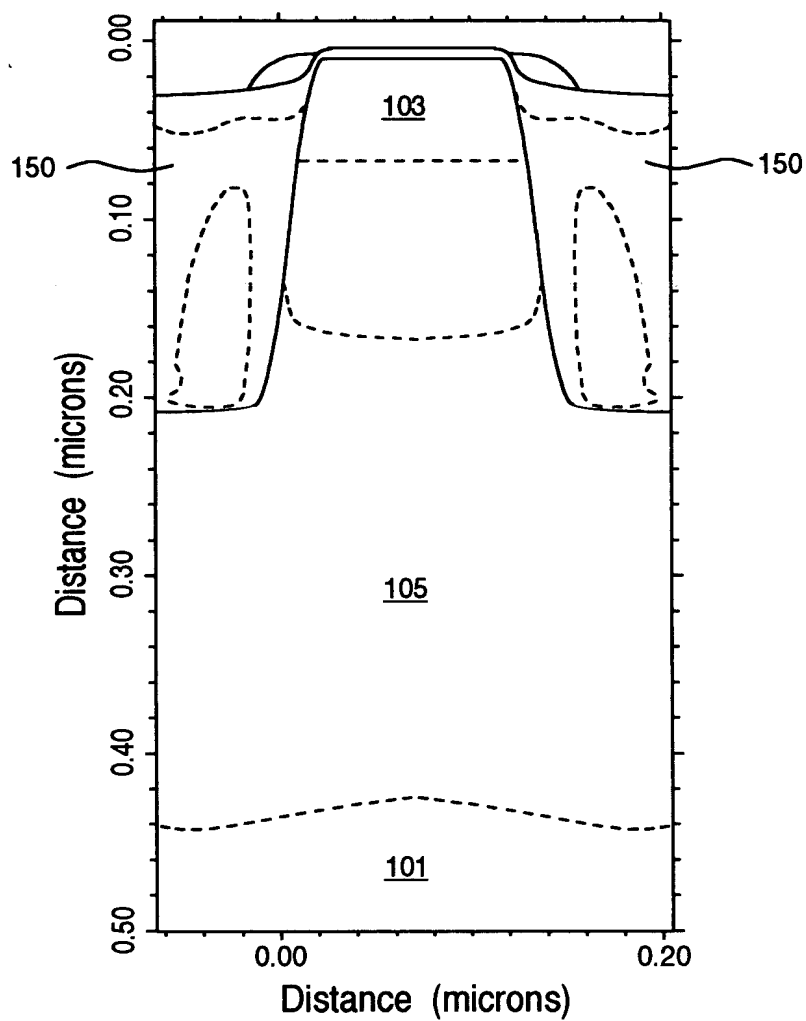


Fig. 1A
PRIOR ART



(Prior Art)

Fig. 1B

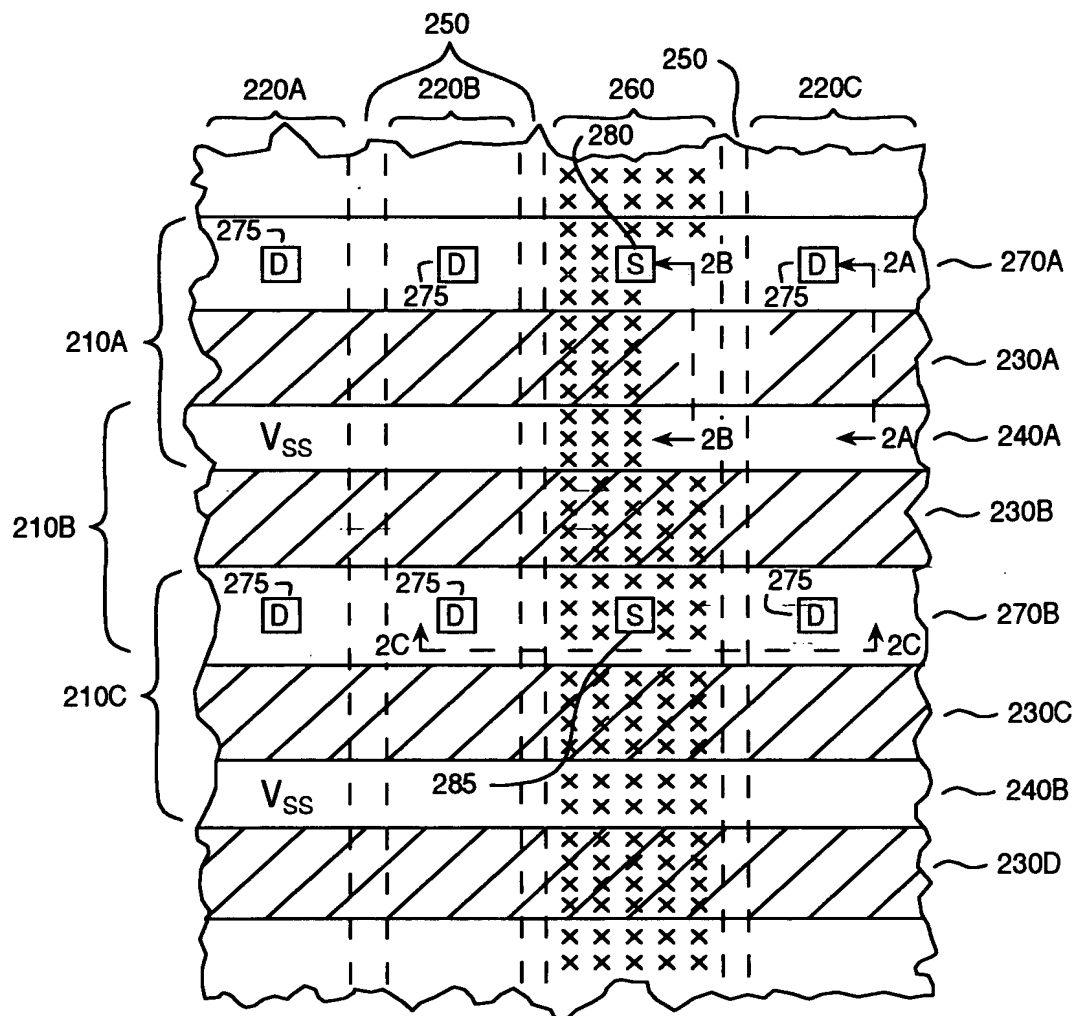


Fig. 2

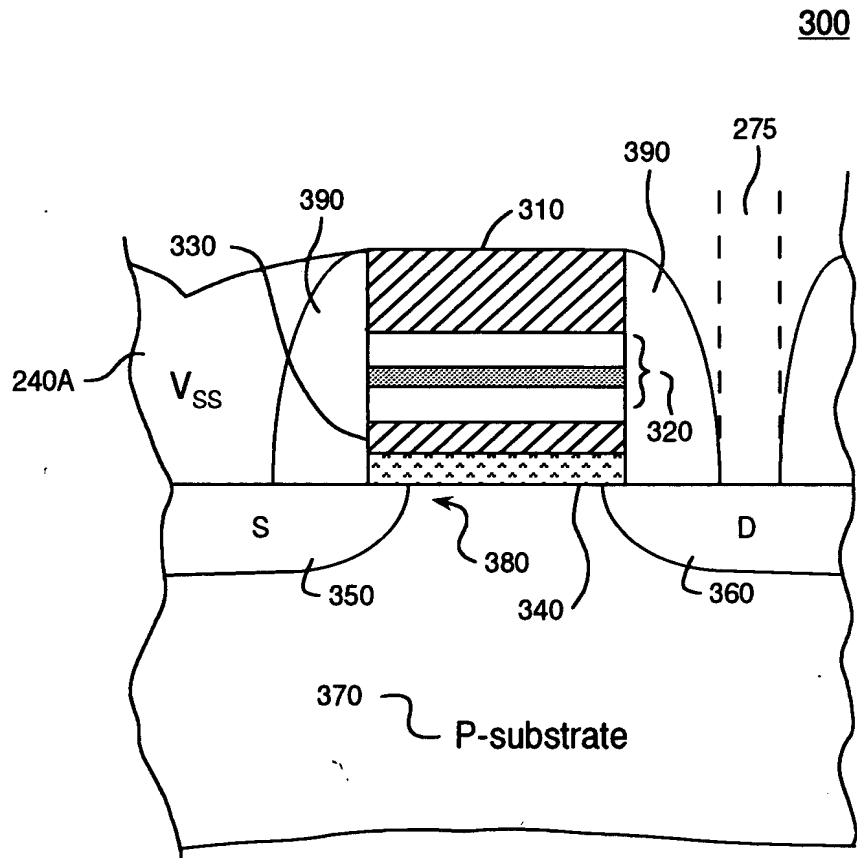


Fig. 3

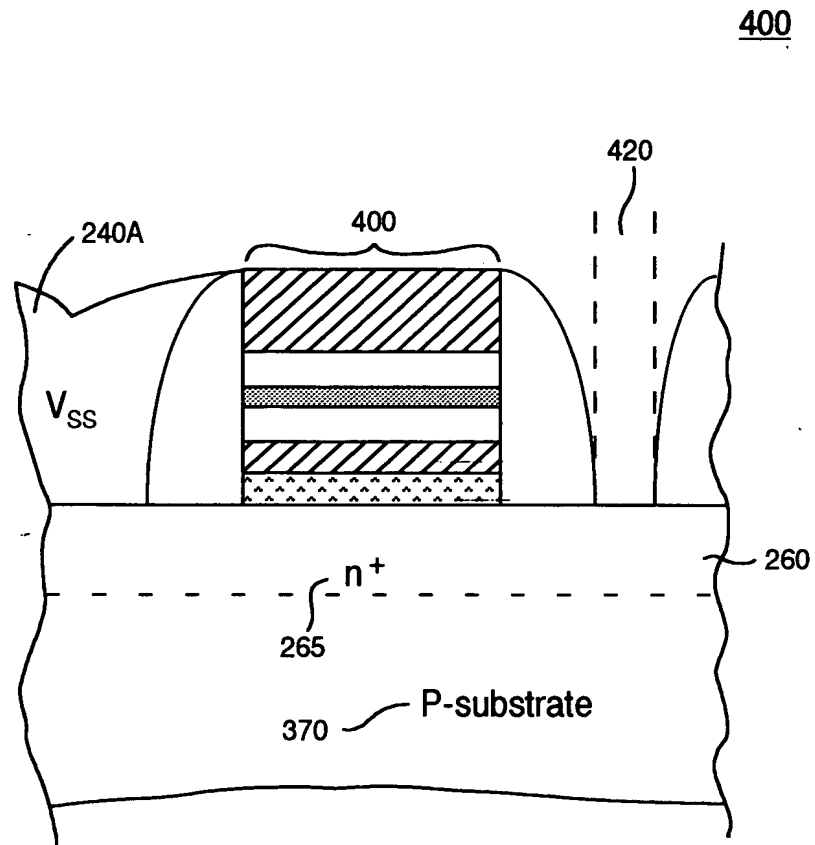


Fig. 4

500

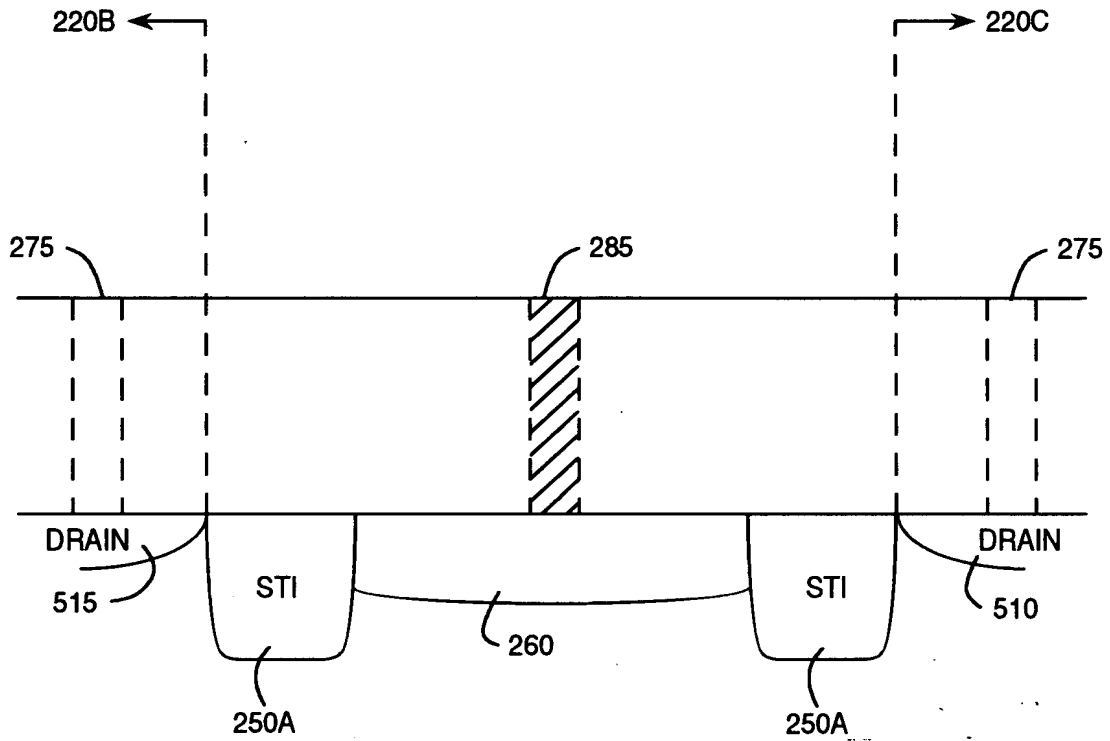


Fig. 5

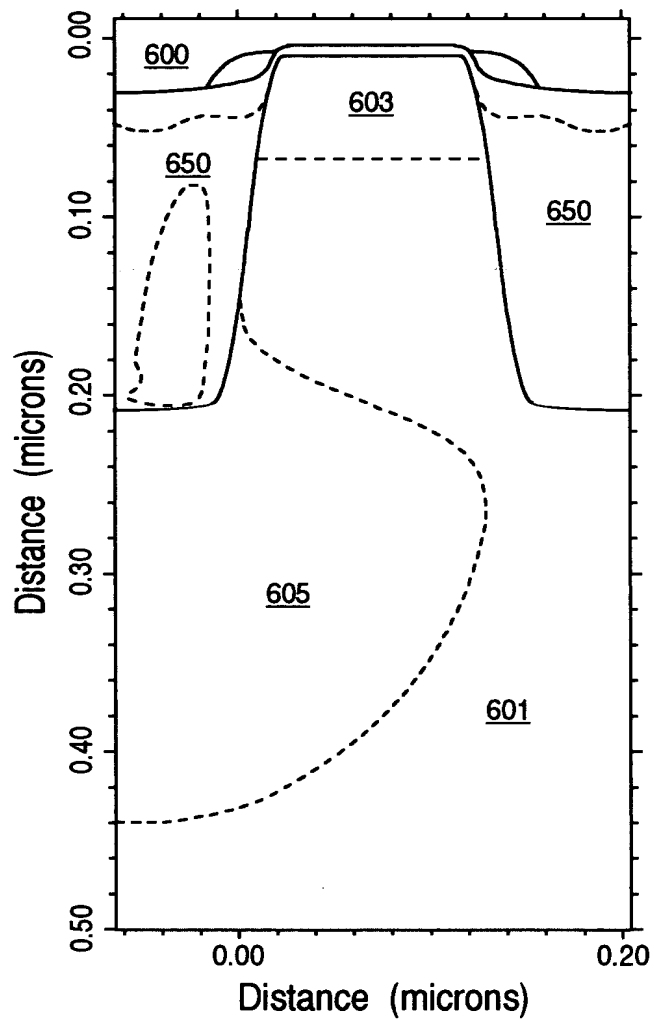


Fig. 6

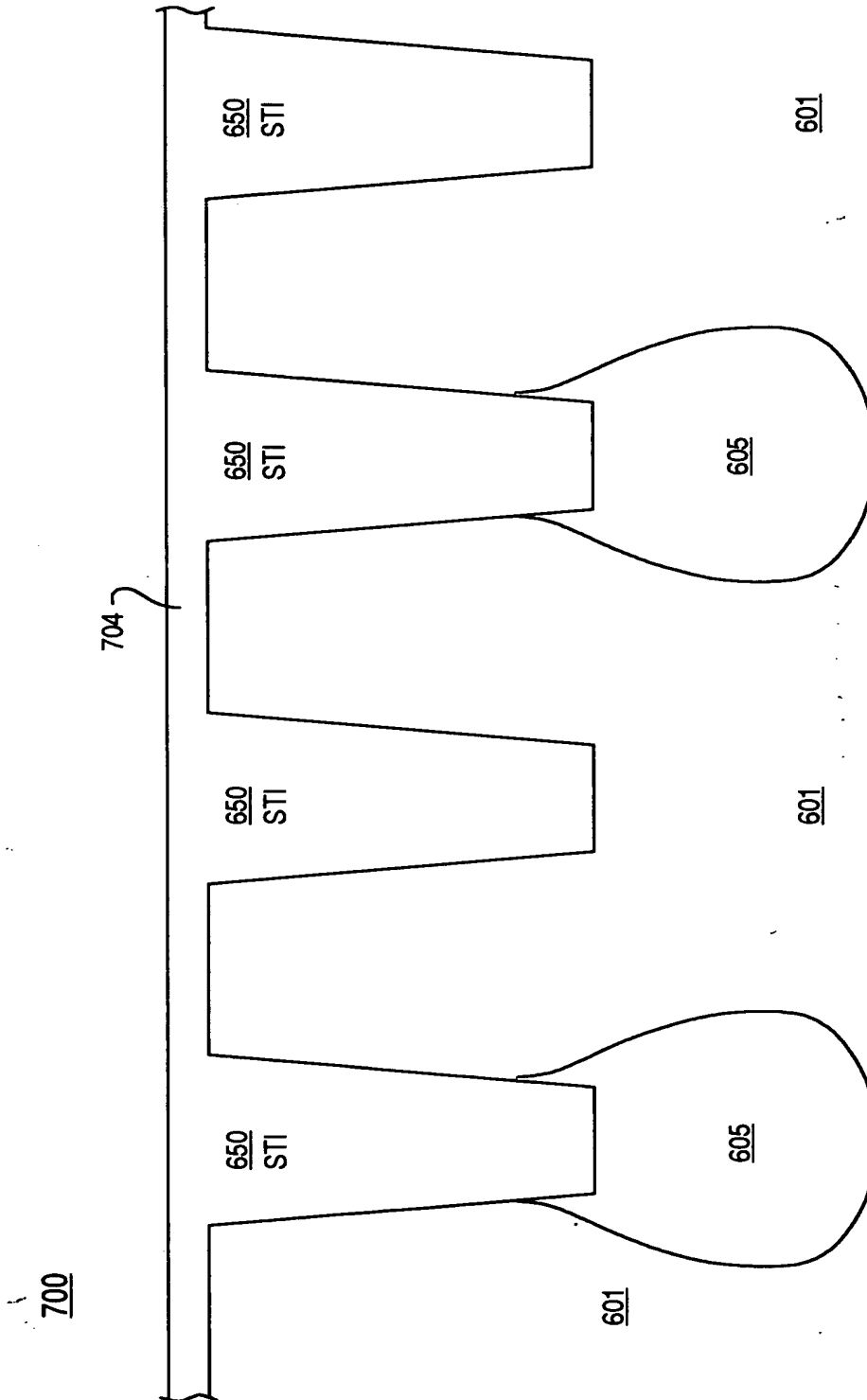


Fig. 7

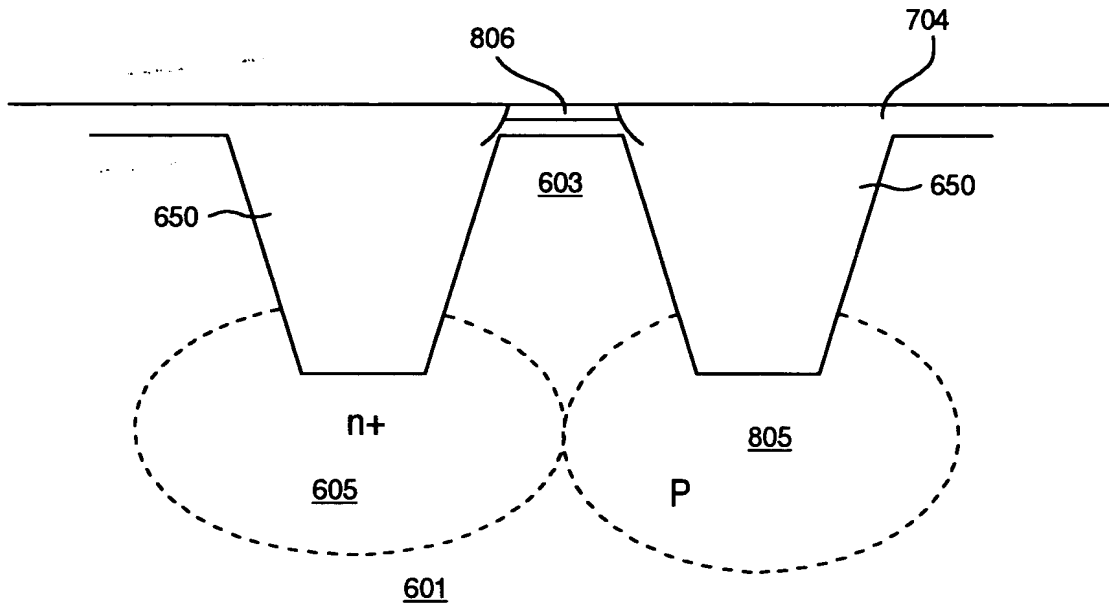


Fig. 8

900

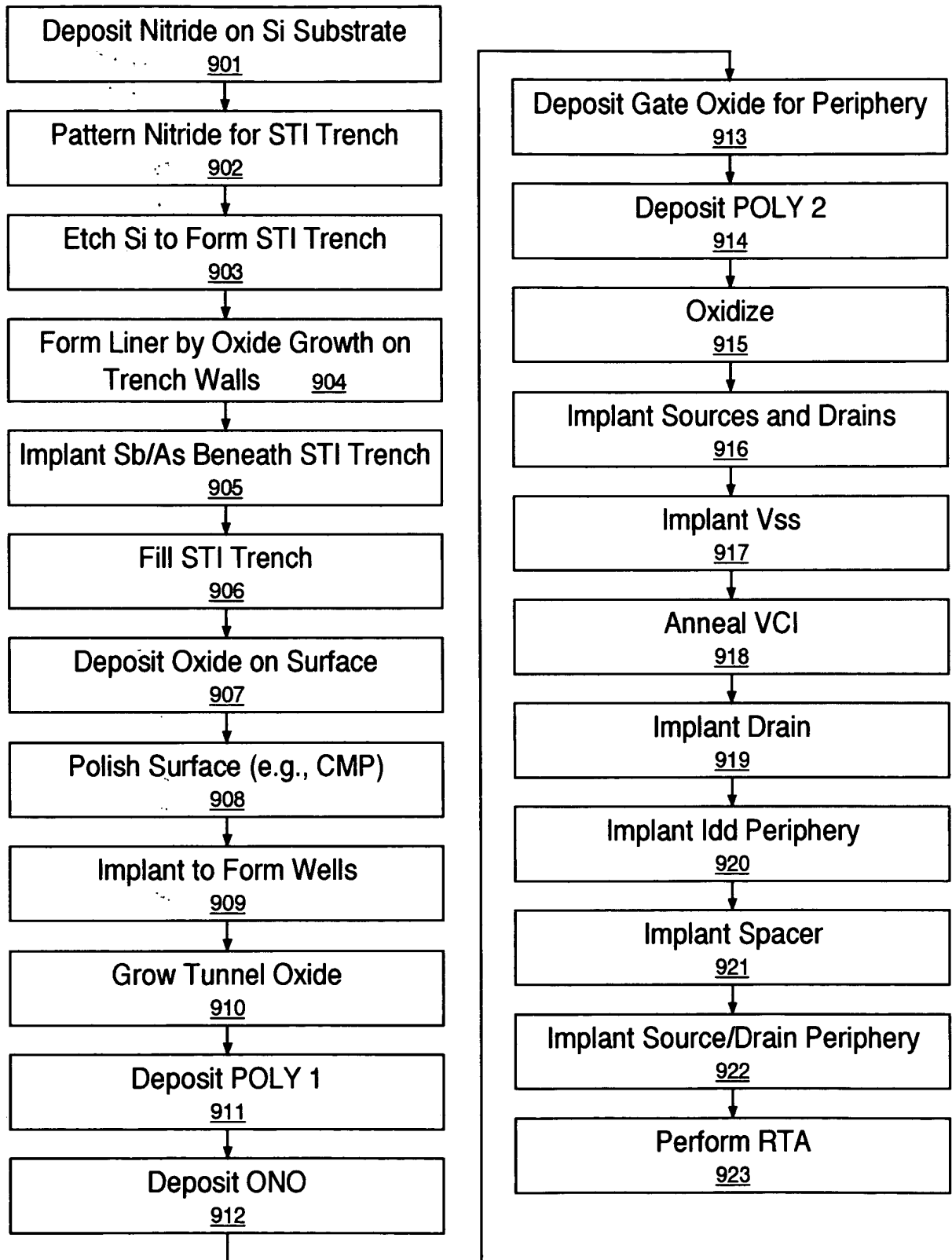


Fig. 9